## Features

- Low Insertion Loss: 0.40 dB @ 0.9 GHz
- High Isolation: 28 dB @ 0.9 GHz
- Harmonics: <-65 dBc
- Low DC Power Consumption
- Low Cost SOT-26 Using Lead (Pb) free materials with RoHS compliant


## Description

The HWS421 is a GaAs MMIC SPDT high power switch in a low cost SOT-26 plastic lead (Pb) free package. The HWS421 features low insertion loss with very low DC power consumption. This high power switch can be used in GSM and PCS systems as selection of transmit or receive function for a common antenna.

SOT-26


Electrical Specifications at $25^{\circ} \mathrm{C}$ with $\mathbf{0 , + 3 V}$ Control Voltages

| Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $\mathrm{DC}-1.0 \mathrm{GHz}$ <br> $\mathrm{DC}-2.0 \mathrm{GHz}$ |  | 0.40 <br> 0.50 | 0.60 <br> 0.70 | dB <br> dB |
| Isolation | $\mathrm{DC}-1.0 \mathrm{GHz}$ <br> $\mathrm{DC}-2.0 \mathrm{GHz}$ | 25 <br> 19 | 27.5 <br> 21.5 | dB <br> dB |  |
| VSWR | $\mathrm{DC}-2.0 \mathrm{GHz}$ |  | $1.20: 1$ |  |  |
| Input Power for One dB Compression | $0.5-2.0 \mathrm{GHz}$ |  | 38 |  | dBm |
| $2^{\text {nd }} \& 3^{\text {rd }}$ Harmonics | $34 \mathrm{dBm} @ 1 \mathrm{GHz}$ |  | 70 |  | dBc |
| Switching Time |  |  | 200 |  | ns |
| Control Current |  |  |  | 100 | uA |

Note: All measurements made in a 50 ohm system with $0 /+3.0 \mathrm{~V}$ control voltages, unless otherwise specified.

Typical Performance Data @ $+25^{\circ} \mathrm{C}$

## Insertion Loss vs Frequency



Isolation vs Frequency


Return Loss vs Frequency


Harmonic Rejection @ 34 dBm, 1 GHz


## Absolute Maximum Ratings

| Parameter | Absolute Maximum |
| :--- | :---: |
| RF Input Power <br> $0.5-2.5 \mathrm{GHz}$ | +38 dBm |
| Control Voltage | +6 V |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

## Pin Out (Top View)



DC blocking capacitors $C_{B}$ are required on all RF ports.
$C_{B}=C_{A}=51 \mathrm{pF}$ for operating frequency $>500 \mathrm{MHz}$.

Logic Table for Switch On-Path

| VC1 | VC2 | RFC-RF1 | RFC-RF2 |
| :---: | :---: | :---: | :---: |
| 1 | 0 | Insertion Loss | Isolation |
| 0 | 1 | Isolation | Insertion Loss |


| $\prime 1^{\prime}=+3 \mathrm{~V}$ to +5 V |
| :--- |

